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SPECIFICATION FOR SMT LVPECL OSCILLATOR MtronPTI P/N M6300S010

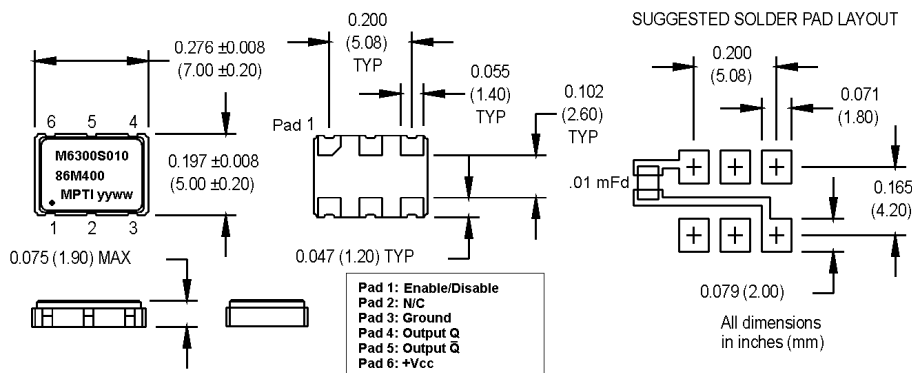
I. GENERAL & ELECTRICAL REQUIREMENTS:

1. FREQUENCY OF OPERATION: 86.400000 MHz
2. FREQUENCY STABILITY OVER OPERATING TEMPERATURE RANGE: ± 1 ppm max.
3. FREQUENCY VS. AGING: ± 3 ppm max. first year. ± 1 ppm/year max. thereafter
4. OPERATING TEMPERATURE RANGE: -40°C to $+85^{\circ}\text{C}$
5. OPERATING VOLTAGE (V_{CC}): $3.3\text{ V} \pm 5\%$
6. OPERATING CURRENT: 125 mA max.
7. OUTPUT TYPE: Differential LVPECL
8. SYMMETRY: 45/55% ref. to 50% of waveform
9. RISE/FALL TIME: 0.55 nS max. ref. to 20% to 80% of waveform
10. OUTPUT LOGIC LEVELS: $V_{OL} = V_{CC} - 1.63\text{ V}$ max. $V_{OH} = V_{CC} - 1.02\text{ V}$ min.
11. OUTPUT LOAD: 50 ohms terminated to $V_{CC} - 2\text{ VDC}$
12. PHASE JITTER (RMS): 1 pS RMS max. (12 kHz to 20 MHz)
13. ENABLE/DISABLE FUNCTION (Pad 1): Logic "1" enables outputs.
Logic "0" disables outputs.
14. PHASE NOISE (Typical): 10 Hz -70 dBc/Hz , 100 Hz -90 dBc/Hz , 1 kHz -120 dBc/Hz
10 kHz -135 dBc/Hz , 100 kHz -140 dBc/Hz , 1 MHz -145 dBc/Hz , 10 MHz -150 dBc/Hz

II. ENVIRONMENTAL/MECHANICAL REQUIREMENTS:

1. SHOCK: MIL-STD-202, Method 213, Condition C.
2. VIBRATION: MIL-STD-202, Methods 201 & 204.
3. THERMAL SHOCK: Per MIL-STD-883, Method 1010 B.
4. HERMETICITY: 1×10^{-8} atm cc/sec min.
5. STORAGE TEMPERATURE: -55°C to $+125^{\circ}\text{C}$
6. SOLDERABILITY: Per EIAJ-STD-002
7. MAXIMUM SOLDERING CONDITIONS: See Figure 1.
8. PACKAGE: 6- pad leadless ceramic 5 X 7 mm, RoHS compliant.

III. DIMENSIONS:



IV. DATA SHEET REVISION TABLE:

Date	Rev.	Author	Details of Revision
3/11/09	0	WNJ	Original release.