



SPECIFICATION FOR RoHS 6 COMPLIANT LVPECL OUTPUT SMT VCXO MtronPTI P/N M3100S106

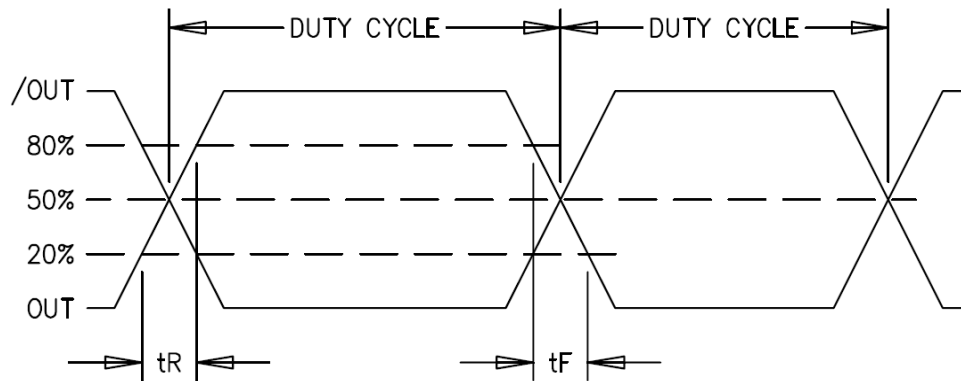
Electrical Specifications:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Frequency of Operation	F _O		120.000000		MHz	
Frequency Stability						
vs Temperature	ΔF/F	-50		+50		
vs. Aging		-5		+5	ppm	1 st year
		-1		+1		Per year thereafter.
RF Output						
Output Type		LVPECL Compatible				
Output Load		50 Ω (V _{CC} - 2)				
Symmetry (duty cycle)	T _{DC}	45		55	%	Ref. to V _{CC} - 1.3 V
Output Logic	V _{OH}	V _{CC} - 1.02			V	Logic Level “1”
	V _{OL}			V _{CC} - 1.63	V	Logic Level “0”
Rise/Fall Time	T _R /T _F			0.55	nS	From 20% to 80% of Waveform
Enable/Disable Logic		80% V _{CC} or N/C			V	Pin 2: Output Enabled
				0.5	V	Pin 2: Output to high-Z
Frequency Adjustment						
Control Voltage Range		0.3	1.65	3.0	V	Pin 1
Pullability (APR)		± 150			ppm	Pin 1
Linearity				5	%	Positive Monotonic
Modulation Bandwidth		10			kHz	- 3 dB
Input Resistance	R _{IN}	500			kΩ	Pin 1
Other Parameters						
Phase Noise			-70		dBc/Hz	@ 100 kHz
			-100			@ 1 kHz
			-122			@ 10 kHz
			-133			@ 100 kHz
			-143			@ 1 MHz
			-150			@ 10 MHz
Phase Jitter (RMS)	Φ _J		0.5		ps	12 KHz to 20 MHz
Supply Voltage & Power Consumption						
Operating Voltage	V _{CC}	3.135	3.3	3.465	V	
Operating Current	I _{CC}			125	mA	



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Output Waveform:



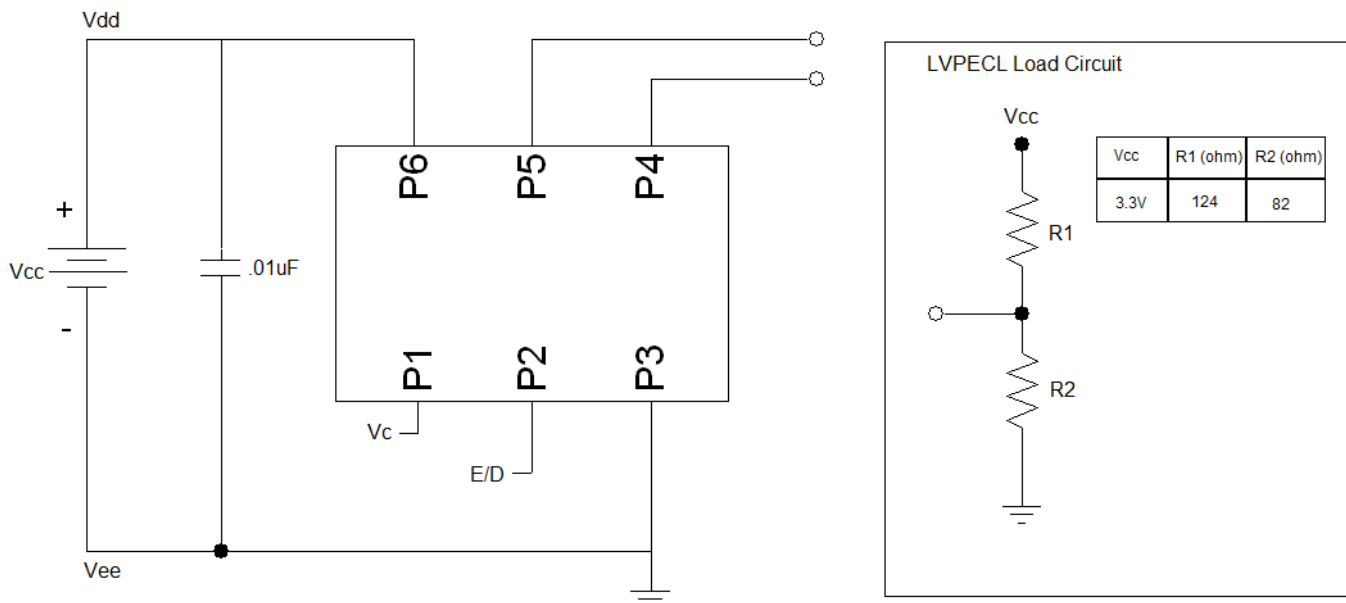
Environmental Conditions:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Operating Temperature	T_A	0		+70	°C	
Storage Temperature	T_S	-55		+125	°C	
Shock	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, 1/2 sinewave)					
Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)					
Thermal Cycle	Per MIL-STD-883, Method 1010, B (-55°C to 125°C, 15 min. dwell, 10 cycles)					
Hermeticity	Per MIL-STD-202, Method 112 (1 x 10 ⁻⁸ atm cc/s of Helium)					
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Moisture Sensitivity Level (MSL)	MSL 1					
Solderability	Per EIAJ-STD-002					
Max. Soldering Conditions	See Figure 1.					
Package Type	5x7 6-pad leadless ceramic. RoHS compliant.					



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Typical Test Circuit & Load Circuit Diagrams:



Soldering Conditions:

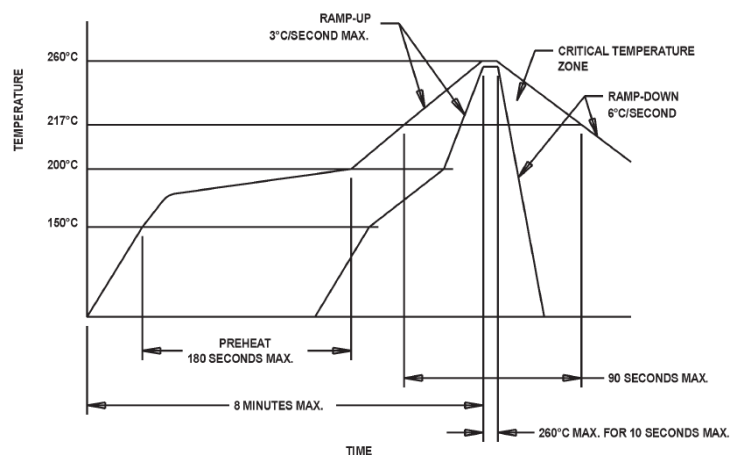


Figure 1



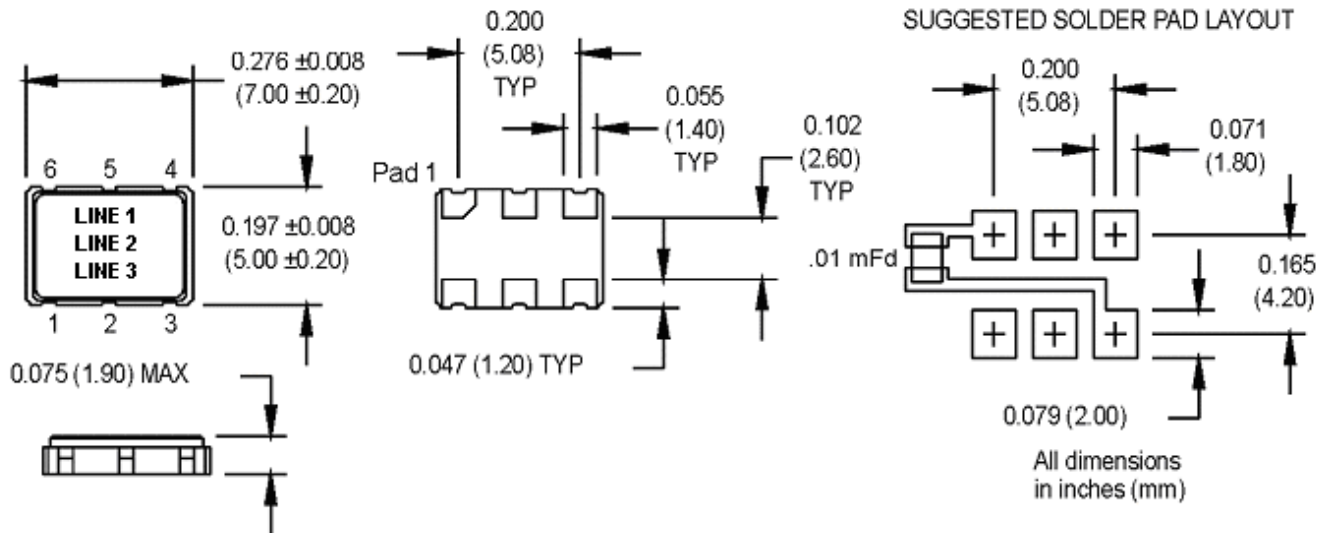
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Mechanical, Marking, and Pin Out Information:

Part Marking	
Line 1	M3100S106
Line 2	120M0000
Line 3	MPTI yyww

Legend	
yy	Year
ww	Work week

Pin	Function
1	Voltage Control
2	Enable/Disable
3	Ground
4	Output
5	Complementary Output
6	+V _{CC}



Datasheet Revision Table:

Date	Rev.	Author	Details of Revision
11/02/18	0	MM	Original release.